What is Claimed is:

1. A method for setting system working frequency, comprising steps of:

    a. executing computer reset and asserting reset;

    b. determining whether setting of a jumper-free IC has been made and whether previous computer initialization was successful; if the outcome is positive, modulating the system working frequency according to set values of BIOS through the jumper-free IC; if the outcome is negative, proceeding next step;

    c. deasserting reset and starting CPU; and

    d. proceeding and completing subsequent initialization process.

2. The method of claim 1, wherein the jumper-free IC at step b is an application specific integrated circuit (ASIC) for modulating the system working frequency through controlling the voltage value of a jumper.

3. The method of claim 1, wherein after step c is completed, determining whether the setting of the jumper-free IC being matched the setting of the BIOS; if the settings do not match, writing the BIOS setting into the jumper-free IC and branching to step a to do execution of the assert reset one more time; if the settings matched, proceeding step d.

4. A method for setting system working frequency, comprising steps of:

    a. executing computer reset and asserting reset;

    b. determining whether setting of a jumper-free IC has been made; if the outcome is positive, modulating the system working frequency according to set values of BIOS through the jumper-free IC; if the outcome is negative, proceeding next step;

    c. deasserting reset and starting CPU; and

    d. proceeding and completing subsequent initialization process.

5. The method of claim 4, wherein the jumper-free IC at step b is an application specific integrated circuit (ASIC) for modulating the system working frequency through controlling the voltage value of a jumper.

6. The method of claim 4, wherein after step c is completed, determining whether the setting of the jumper-free IC being matched the setting of the BIOS; if the settings

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do not match, writing the BIOS setting into the jumper-free IC and branching to step a to do execution of the assert reset one more time; if the settings matched, proceeding step d.

7. A method for setting system working frequency, comprising steps of:

    a. executing computer reset and asserting reset;

    b. determining whether previous computer initialization was successful; if the outcome is positive, modulating the system working frequency according to set values of BIOS through the jumper-free IC; if the outcome is negative, proceeding next step;

    c. deasserting reset and starting CPU; and

    d. proceeding and completing subsequent initialization process.

8. The method of claim 7, wherein the jumper-free IC at step b is an application specific integrated circuit (ASIC) for modulating the system working frequency through controlling the voltage value of a jumper.

9. The method of claim 7, wherein after step c is completed, determining whether the setting of the jumper-free IC being matched the setting of the BIOS; if the settings do not match, writing the BIOS setting into the jumper-free IC and branching to step a to do execution of the assert reset one more time; if the settings matched, proceeding step d.